

Name of the Faculty : Dr. Pradeep C

Designation : Professor & Principal

E-mail ID : principal@mbcpeermade.com

Education Summary : Ph.D. from Anna University, Chennai.
M.Tech. from Bharath University, Chennai.
B.E. from Madras University, Chennai



Area of Interest : Testing VLSI Circuits
Advanced Digital Design
CMOS VLSI Design
Microprocessors & Microcontrollers
Linear Integrated Circuits

Employment History : Working as Professor and Principal at MBC College of Engineering & Technology, Peermade, from January 2016.
Worked in various designation like Associate Professor and Head SCIE, Asst. Professor & Lecturer at SAINTGITS College of Engineering, Kottayam, from March 2007 to Dec 2015.

Worked as Lecturer at Amal Jyothi College of Engineering, Kanjirapally, from January 2005 to Aug 2006.

Worked as Lecturer at College of Engineering, Kidangoor, from November 2001 to August 2002.

Worked as Lecturer at Mar Augusthinose College, Ramapuram, from January 1999 to August 2000.

Worked as Shift Operator-Control Room, 110 KV Substation, Erattupetta , Kerala State Electricity Board, from December 2001 to June 2003

Worked as Manager (Training), Computer Park ,Erattupetta, from September 2000 to November 2001

Journals Published : 1. Saranya R, Pradeep C, Radhakrishnan R., Design and Implementation of a Reconfigurable Finite Impulse Response Filter for Adaptive Systems. International Journal of Computational Systems Engineering (IJCSyE). Accepted & In Production. Inderscience Publishers.

2. Jisha, M., Pradeep,C., Intelligent Selective Modular Redundancy for Online Fault Detection of Adders in FPGA. International Journal of High Performance Systems Architecture (IJHPSA). Accepted & In Production. Inderscience Publishers.
3. Varghese, Anila Ann, and C. Pradeep. "FPGA implementation of area-efficient single precision floating point complex divider with fault detection." International Journal of Computational Systems Engineering 2.3 (2016): 174-181. Inderscience Publishers.
4. Eapen, M.E., Pradeep, C., Varghese, A.A. and Nair, J.M., 2016. Placement Strategies for Faulty Cells in Module Relocation Based BISR Approach. Innovations in Bio-Inspired Computing and Applications (pp. 437-446). Springer International Publishing.
5. Anjana, S., Pradeep, C. and Samuel, P., 2015. Synthesize of High Speed Floating-point Multipliers Based on Vedic Mathematics. Procedia Computer Science, 46, pp.1294-1302. Elsevier Publishing.
6. Baby, N., Pradeep, C., Saranya, R. and Radhakrishnan, R., 2015. Synthesis of Reconfigurable Video Compression Modules in Virtex FPGAs for Multiple Fault Repair Mechanism. Procedia Computer Science, 46, pp.1333-1340. Elsevier Publishing.
7. Saranya, R., Pradeep, C., Baby, N. and Radhakrishnan, R., 2015. FPGA Synthesis of Reconfigurable Modules for FIR Filter. International Journal of Reconfigurable and Embedded Systems (IJRES), 4(2).
8. Pradeep, C, Radhakrishnan, R & Philip Samuel 2014, 'Fault Recovery Algorithm Using King Spare Allocation and Shortest Path Shifting for Reconfigurable Systems', Journal of Theoretical and Applied Information Technology, vol. 61, no.2, pp 254-261.
9. Pradeep, C, Radhakrishnan, R 2014, 'FPGA Evaluation of Reconfigurable Modules with Self Repair Mechanism', International Journal of Reconfigurable and Embedded Systems, vol.3, no.2,pp.1-12.
10. Pradeep, C, Radhakrishnan, R, Saranya, R & Philip Samuel 2014, 'Area Efficient Data Path with Online Fault Detection Mechanism for Reconfigurable Systems', Australian Journal of Basic and Applied Sciences, vol.8, no.10, pp. 239-245.

11. Pradeep, C, Radhakrishnan, R, Neena Baby & Philip Samuel, 'Multi objective Built in Self Repair Algorithm with Multiple Fault Detection for Reconfigurable Systems' ,Journal of Theoretical and Applied Information Technology, vol. 69, no.2,pp.248-256.
12. Pradeep, C, Radhakrishnan, R 2014, 'Fault Detection Methods for Interconnects of Reconfigurable Hardware', I-manager's Journal on Embedded systems, vol.4, no.2, pp.1-11.
13. Pradeep C."Design and Implementation of Reconfigurable LFSR "International Journal on Information and Communication Technologies, Volume 2, June 2009 (pp 139-142).
14. Reshma Mary John, Pradeep C., 2013,"Self-Repairing Algorithm with Shared Spare Allocation for Reconfigurable Systems", International Journal of Emerging Technology and Advanced Engineering. Volume 3, Issue 8, pp 716-721.
15. Ajith Ravindran, Soya Treesa Jose and Pradeep C"A 1.5V Area Efficient Asynchronous Adder using MODL and Double Pass Transistor Logic" International Journal of Scientific & Engineering Research, Volume 4, Issue 8, August 2013

**Proceedings in
International/National
Conferences**

1. Baby, N. and Pradeep, C., 2014, July. FPGA partitioning and synthesis of reconfigurable video compression module. In Control, Instrumentation, Communication and Computational Technologies (ICCICCT), 2014 International Conference on (pp. 360-364). IEEE Xplore.
2. Saranya, R. and Pradeep, C., 2014, July. FPGA synthesis of area efficient data path for reconfigurable FIR filter. In Control, Instrumentation, Communication and Computational Technologies (ICCICCT), 2014 International Conference on (pp. 349-354). IEEE Xplore.
3. Anjana, S. and Pradeep, C., 2014, July. High speed integer multiplier designs for reconfigurable systems. In Control, Instrumentation, Communication and Computational Technologies (ICCICCT), 2014 International Conference on (pp. 393-397). IEEE Xplore.

- : 4. Pradeep, C, Radhakrishnan, R & Philip Samuel 2014, 'Reduced Time Testing Method for Permanent Faults in Interconnects of Reconfigurable Hardware', Proceedings of International Conference On Systemic, Cybernetics and Informatics, vol. 1 & 2, pp. 018-022.
5. Pradeep C, "Design and Implementation of Reconfigurable LFSR" Proceedings of International Conference ICVCom 09, SAINTGITS College of Engineering, Kottayam. (pp 315-318).
6. Pradeep C, "Design and Implementation of 32 bit RISC Processor in FPGA" Proceedings of National Conference NCACS 2009, SJCET, Pala, Kottayam. pp 5-10.
7. Pradeep C, "Verilog HDL implementation of Superscalar Processor with Speculative branch Prediction", Proceedings of National Conference NC-(ET) 2, SAINTGITS College of Engineering, Kottayam. pp 315-318.
8. Pradeep C, NIMISHA SUBHASH, RESHMA MARY JOHN, 2013. "Permanent Fault Detection Method for Interconnects in Reconfigurable Systems", Proceedings of U.G.C Sponsored III National Conference on Modern Trends in Electronic Communication & Signal Processing.
9. Reshma Mary John, Pradeep C., 2013. "Responsive Back-Up Circuits (RBC) Inspired Fault-Recovery Algorithm for Reconfigurable Systems", Proceedings of U.G.C Sponsored III National Conference on Modern Trends in Electronic Communication & Signal Processing
10. Ajith Ravindran, Soya Treesa Jose and Pradeep C "A 1.5V Area Efficient Asynchronous Adder using MODL and Double Pass Transistor Logic" Proceedings of International Conference on Global Innovation in Technology and Sciences (ICGITS 2013), 4-6th April 2013.
11. Jose, S.T. And Pradeep, C, 2013 "Design of a multichannel NAND Flash memory controller for efficient utilization of bandwidth in SSD's "proceedings of International Multi-Conference on Automation, Computing, Communication, Control and Compressed Sensing (iMac4s), 22-23 March 2013, Kottayam, India. pp 235 - 239. IEEE Xplore

12. Oommen,D. And Pradeep,C.,2012 "Reconfigurable router using RLBS algorithm " Proceedings of 12th International Conference on Intelligent Systems Design and Applications (ISDA). 27-29 Nov.2012, Kochi, India. pp 332 - 336. IEEE Xplore
13. Aith Ravindran And Pradeep C.,2012"1.5v, .18u Area Efficient 32 Bit Adder using 4T XOR and Modified Manchester Carry Chain " Proceedings of 12th International Conference on Trends in Innovative Computing 2012 - Intelligent Systems Design, 27-29 Nov.2012,Kochi,India.pp 140 - 144.
14. Aith Ravindran And Pradeep C "Area Efficient Quad Bit PTL Adder", In Proc. of National Conference on VLSI and Embedded Systems Technology and Advancements, VESTA2012, Pg. 194 - 198, Oct. 2012.
15. Soya Treesa Jose And Pradeep C, "Design of High Performance Flash Memory Controller," In Proc. of National Conference on VLSI and Embedded Systems Technology and Advancements, VESTA2012, Pg. 28 - 31, Oct. 2012.
16. Dhanya Oommen And Pradeep C,"RLBSA-Adaptive Routing Algorithm for NOC," In Proc. of National Conference on VLSI and Embedded Systems Technology and Advancements, VESTA2012, Pg. 60 - 64, Oct. 2012.
17. Mathew, B.K., John, S.K., Pradeep, C., 2008 "New Technique for Fault Detection in Quantum Cellular Automata "Proceedings of First International Conference on Emerging Trends in Engineering and Technology (ICETET '08),16-18 July 2008,Nagpur, Maharashtra, India. pp 834 - 837. IEEE Xplore.
18. Binu K Mathw, Pradeep C And Shajimon K John, "DFT Techniques for opens in CMOS latches" Proceedings of First International Conference on Advances in Computing, Chikhli, India, 21-22 February 2008.

Training attended

- : 1.Innovation-led and Technology-Intensive Entrepreneurship: The Way Ahead for Kerala.3rd Dec. 2013,by Kerala Financial Corporation at Mascot Hotel, TVPM
- 2. ISTE e-Workshop on 'Creative Teaching'', 5th Oct 2013, SAINTGITS College of Engineering, Kottayam.

3. Workshop on 'Patents ', 27th - 28th Sept, 2013, Amal Jyothi College of Engineering, Kanjirapally.

4. ISTE workshop on 'Software Development Techniques for Teachers of Engineering and Science Institutes', Nov-5th Nov, 2011, SAINTGITS College of Engineering, Kottayam.

5. National Faculty Development Program 'Entrepreneurship', Oct 19th -30th, 2010, Technopark, Trivandrum.

6. National Workshop on 'Opportunities of Government Funded R&D Projects for Academic Institutions', 06-07 Nov 2009, Toc H Institute of Science and Technology.

7. STTP program on 'Research Trends in Embedded Systems and Signal Processing', 28th Oct-3rd Nov 2009, SAINTGITS College of Engineering, Kottayam.

Professional Membership

IEEE Member
: ISTE Life Member
ACM Member
IAENG Member
UACEE Senior Member

Editor/Reviewer Journals/ Examiner/Conference Chair

: Reviewer in International Journal of Computational Systems Engineering, Inderscience Publishers.
Reviewer in International Journal of Information and Communication Technology, Inderscience Publishers.
Reviewer in International Journal of Embedded Systems, Inderscience Publishers.
Chairperson cum Session chair for various National and International Conferences.
Examiner of PhD thesis evaluation.