

Name of the Faculty

: Prof. Anish M. George



Designation

: Assistant Professor

E-mail ID

: anishmgeorge@mbcpeermade.com

Education Summary

: M.Tech. from MG University
B.Tech. from MG University

Area of Interest

: Digital Electronics and Digital System Design
VLSI & Embedded Systems
Digital Signal Processing

Worked as Guest Lecturer at M G University School of Technology and Applied Science , Kottayam from September 2013 to January 2014

Working as Asst.Professor in MBC College of Engg& Tech from 2014 to till date.

Worked as Systems Engineer at Infosys Technologies Ltd from 2009-2011

Journals Published

: "Design and Analysis of Power and Area Efficient 2/3 Prescaler Using E-TSPC Logic" in the International Journal of Scientific and Engineering Research (IJSER) August 2013.

International/National Conferences / Workshops / Seminars attended

1. FDP on CAD Tools in VLSI and Communication at SAINTGITS College of Engineering from 28.11.2014 to 05.12.2014
2. National Conference at Sree Buddha College of Engineering on 06.09.2013 and presented on Low Power and High Speed 2/3 Prescaler Design using E-TSPC logic
3. National Conference at Musaliar College of Engineering and Technology on 04.05.2013 and presented on Low Power and High Speed 2/3 Prescaler using E-TSPC logic

4. International Conference on Global Innovations in Technology and Sciences (ICGITS 2013) at SAINTGITS College of Engineering from 04.04.2013 to 06.04.2013

5. ISTE Workshop on Introduction to Research Methodologies at SAINTGITS College of Engineering organized by IIT Bombay from 25.06.2012 to 04.07.2012

6. National Workshop on “VLSI Signal Processing” at Musaliar College of Engineering and Technology from 01.03.2012 to 02.03.2012

Training attended:

- : 1. Training on Signal Processing and Control Systems at MBC College of Engineering and Technology, Peermade from 31.01.2014 to 01.02.2014

Professional Membership

- : ISTE Life Member