

Name of the Faculty

: Prof.Arjun Hari M



Designation

: Assistant Professor

E-mail ID

: arjunharim@mbcpeermade.com

Education Summary

: M.Tech. from Amrita Vishwa Vidyapeetham
University,Amritapuri
B.Tech. from MBC CET Peermade

Area of Interest

: VLSI testing
Circuit Analysis and Design
Signal processing

Employment History

: Working as Assistant Professor in M.B.C College Of
Engineering And Technology,Peermade from 2011
June 1st to present

Journals Published

: Analysis of Fixed Width and Flexible Width TBA and
TRA Test Architectures for Soc Design Int. J. on Recent
Trends in Engineering and Technology, Vol. 6, No. 1,
Nov 2011 Short Paper

**International/National
Conferences / Workshops /
Seminars attended**

: Attended a seminar on Employability of fresh
engineering graduates in the era of globalization at
Nodal Centre for Kerala-National technical Manpower
Information System during 24-25 January 2012

Attended a workshop on Continuing Education
Congress at IEEE GOLD -Kerala Chapter,SJCET PALA
during 8-12 October 2012

Attended a workshop on Control Systems at IIST
,Thiruvanthapuram on 12 December 2012

Attended a FDP on Signal Processing And Control
Systems at MBC College Of Engineering And
Technology,Peermade during 31st December 2013-1st
January 2014

Attended a Workshop on Entrepreneurship at NSTEDB, Department Of Science & Technology, Government Of India during 5 -17 May 2014

Attended a NPTEL WORKSHOP at NPTEL (IIT Madras) at AmalJyothi College Of Engineering on 21st November 2015

Attended a Conference at APJ ABDUL KALAM TECHNOLOGICAL UNIVERSITY on "Entrepreneurship - A National Imperative" on 11th June 2016

Professional Membership : ISTE Life Member