



- Name of the Faculty** : Prof. Deepa Susan George
- Designation** : Assistant Professor
- E-mail ID** : deepasusangeorge@mbcpeermade.com
- Education Summary** : M.Tech from SRM University  
B.Tech from M.G University
- Area of Interest** : Low Power VLSI  
Verilog Programming
- Employment History** : Worked as Asst. Professor in Mangalore Institute of Technology & Engineering from July, 2013 to Dec 2013  
Working as Asst. Professor in MBC College of Engg & Tech from 2014 to till date.
- Journals Published** : Deepa Susan George , V. Sarada "Low power ROM less FFT Processor" in International Journal IJACTE, ISSN.2319- 2526, Volume-2, Issue-2, 2013
- International/National Conferences / Workshops / Seminars attended** : 1) Attended International Conference on Electrical, Electronics and Computer Science by IT Society of India on 14 April, 2013  
2) Attended National conferences on VLSI & Embedded Systems by SRM University from 14, Feb 2013 to 15 Feb 2013  
3) Attended workshop on Signal Processing and Control Systems at MBC, Peermade from Jan 30, 2014 to Jan 31, 2014  
Attended workshop on Latest Trends in EDA for Communication, Electronics and VLSI Technology at St. Joseph Engineering College, Mangalore from 29, July 2013 to 30, July 2013

**Training attended:**

- : 1)Attended Hands-on training in EDA tools by Cadence Design Systems from 21,Feb 2012 to 23 Feb,2012
- 2)Attended training on RTL Design for Programmable Logic by NIELIT,Calicut from 18 April,2012 to 20 April,2012

**Professional Membership**

- : ISTE Life Member