

Name of the Faculty

: Prof.Roshna T.R

Designation

: Assistant Professor

E-mail ID

: roshnatr@mbcpeermade.com

Education Summary

: M.Tech from Kerala University
B.E from Anna University



Area of Interest

: Real Time Systems
Embedded Systems
VLSI

Employment History

: Worked as Graduate Engineer Trainee in Keltron Electro Ceramics Limited from 2014-2015.
Working as Asst.Professor in MBC College of Engg& Tech from 2015 to till date.

Journals Published

: Roshna TR,Nivin R,Sherly Joy ,Apren TJ and Alex V,2013,Design and Implementation of digital Costas loop and bit synchronizer in FPGA for BPSK demodulation.ICCC.pp.39-44.
Roshna TR,Nivin R and SherlyJoy,2013,Implementation of digital Costas loop in FPGA for BPSK Demodulation. NCETIC13.pp.54-57.

International/National Conferences / Workshops / Seminars attended

: 1)FDP at MBCCET by Muthoot Group on Dec 17, 2015

Training attended:

: Attended 14 days training on FPGA Hardware and Programming at Vi Microsystems Ltd,Chennai

Professional Membership

: ISTE Life Member